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L1: (508) amplifier and inductive adj impedance

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1 and reactive adj impedance

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	U	L	Document ID	Issue Date	Pages	Title	Current ORL	Current XRef	Retrieval Classif	Inventor	S	C	P	SI	NUM
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20040184289 A1	20040923	91	Output resistance modulation in power converters	363/15			Vindrell, Patrizio	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20040183513 A1	20040923	95	Low-loss transformer-coupled gate driver	323/284			Vindrell, Patrizio	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20040174147 A1	20040909	94	Factorized power architecture with point of load sine amplitude converters	323/266			Vindrell, Patrizio	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20040066204 A1	20040408	12	Wafer resistance measurement apparatus and method using capacitively coupled AC excitation signal	324/708			Dorman, Richard	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20030227280 A1	20031211	90	Factorized power architecture with point of load sine amplitude converters	323/265			Vindrell, Patrizio	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20030142513 A1	20030731	108	Factorized power architecture with point of load sine amplitude converters	363/17			Vindrell, Patrizio	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	US 20020175777 A1	20021128	9	Impedance-compensating circuit	333/22R	333/32		Mohwinkel, Clifford A. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20010024450 A1	20010927	12	Method for forming an intermediate frequency signal in a mixer, and a mixer	370/463	370/419		Takalo, Tomi-Pekka et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6670850 B1	20031230	12	Ultra-wideband constant gain CMOS amplifier	330/305	330/141		Roach, Steven D.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6664935 B1	20031216	13	Broad band impedance matching device with coupled transmission lines	343/860	333/124; 333/32; 343/864		Thompson, William John et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6646463 B1	20031111	22	Impedance emulator	326/30	326/21; 326/86; 327/54R		Hariton, Dan Ion	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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